

Electronic Acknowledgement Receipt

EFS ID:	1915365
Application Number:	09775167
International Application Number:	
Confirmation Number:	7275
Title of Invention:	Shift register circuit capable of reducing consumption of power with reduced capacitive load of clock signal line and image display device including it
First Named Inventor/Applicant Name:	Yasushi Kubota
Customer Number:	21874
Filer:	Steven M. Jensen
Filer Authorized By:	
Attorney Docket Number:	55561 (820)
Receipt Date:	27-JUN-2007
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Application Type:	Utility

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Amendment - After Non-Final Rejection	55561response.pdf	23248	no	3

Warnings:

Information:	Total Files Size (in bytes):	23248
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<p>If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.</p>		
<p><u>National Stage of an International Application under 35 U.S.C. 371</u></p>		
<p>If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p>		
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